

# Oracle

## Exam 1z0-489

### SPARC M6-32 and SPARC M5-32 Servers Installation Essentials

Version: 7.0

[ Total Questions: 79 ]

**Question No : 1**

A customer provides you with their NTP server information. In which two places do you need to configure it?

- A. SP
- B. SPP
- C. OBP
- D. Solaris

**Answer: B,D**

**Question No : 2**

Before performing a firmware upgrade, you must first check the keyswitch property for each host. What must it be set to for the firmware upgrade to work?

- A. Normal
- B. Locked
- C. Diag
- D. Open

**Answer: D**

**Question No : 3**

Given a SPARC M5-32 server in which each CMU has 2x M5 processors, each processor has 2x memory controllers, and each memory controller can have 2x BoB, how many memory modules/ sections can there be in each CMU?

- A. 1
- B. 2
- C. 4
- D. 8

**Answer: C**

**Question No : 4**

Assuming that HOST2 is composed of only DCU2, which two targets report the type of CPUs that belong to this DCU?

- A. /System/DCUs/DCU\_2 cpu- summary
- B. /System/Processor type
- C. /Servers/PDomains/PDoma1n\_2/System/Processors summary- description
- D. /Servers/DCU\_2/Processors model

**Answer: A,D**

Reference:

[http://docs.oracle.com/cd/E19102-01/n810.srvr/819-2741-10/subassembly\\_hot\\_replace.html](http://docs.oracle.com/cd/E19102-01/n810.srvr/819-2741-10/subassembly_hot_replace.html)

#### Question No : 5

Which three options are supported memory population options for the SPARC M5-32 server?

- A. CMU with no memory/DIMM populated
- B. CMU with 1/4 populated with DIMM Q
- C. CMU with 1/2 populated with DIMM
- D. CMU with full populated with DIMM
- E. CMU with 1/8 populated with DIMM

**Answer: B,C,E**

#### Question No : 6

On the SPARC M5-32 platform, CMU11 is located in DCU2. If an error is reported on CMU11, which command would you use to identify the PDomain number that has DCU2 assigned?

- A. show /System/DCTs/DCU\_2/CMD\_11 location
- B. show /SySt8m/Processor/cpUs/cro\_22 location
- C. show /Servers/PDomains/\_level 2 \_t dcus\_assigned
- D. show /Servers/PDomains/-level 2 CMU 11

**Answer: B**

**Question No : 7**

You have powered-on HOST2 and want to verify its current status. Which two commands would give that output?

- A. show /Servers/PDomains/PDoraain\_2/HOST status
- B. show /Servers/PDomains/PDomain^l/HOST status
- C. show status /Servers/PDomains /PDomain\_2/HOST
- D. show /Servers/PDomains /PDomain 2/HOST status detail

**Answer: A,D**

Reference:

[http://docs.oracle.com/cd/E50790\\_01/doc/doc.121/e50471/snapshot.htm](http://docs.oracle.com/cd/E50790_01/doc/doc.121/e50471/snapshot.htm)

**Question No : 8**

Which three statements are correct about the front control panel and rear status panel on the SPARC M5-32 server?

- A. The front control panel and rear status panel are hot-swappable.
- B. The Power On/Standby button is located only on the front panel, not on the rear panel.
- C. The System Key switch is located only on the front panel, not on the rear panel.
- D. An antistatic wrist strap connector can be found on both the front and rear panels.
- E. All the control keys mo on the front control panel, and all the LED signals are on the rear status panel.

**Answer: A,C,D**

Reference:

[http://docs.oracle.com/cd/E19095-01/sfv890.srvr/817-3957-12/dak\\_sm\\_preptoservice.html](http://docs.oracle.com/cd/E19095-01/sfv890.srvr/817-3957-12/dak_sm_preptoservice.html)

**Question No : 9**

Before you can configure the service processors, which three pieces of information do you need from the customer?

- A. SP0 IP address
- B. SP1 IP address
- C. domain IP address
- D. Active SP IP address
- E. clock board IP address

**Answer: A,C,D**

Reference:

[https://docs.oracle.com/cd/E19469-01/820-6413-13/IPMI\\_Overview.html](https://docs.oracle.com/cd/E19469-01/820-6413-13/IPMI_Overview.html)

**Question No : 10**

The SPARC M5-32 server is well –suited for many workload requirements. Which requirement does the SPARC M5-32 server best fulfill?

- A. business-critical workloads that scale up to 32 cores
- B. applications that need fast and reliable virtualization technologies with no extra cost
- C. workloads that need encryption without sacrificing application performance
- D. consolidation of mission-critical workloads, where each needs physical isolation domains (PDOMs)

**Answer: D**

**Question No : 11**

After completing an installation, you need to collect the system's configuration. Which two actions do you perform?

- A. Explorer on the SPs
- B. Explorer on the PDOMs
- C. Snapshot on the SPs
- D. Snapshot on the PDOMs

**Answer: B,D**

**Question No : 12**

Which three statements are true about PCIe card population on the SPARC M5-32 server?

- A. Each DCU has two sets of PCIe slots. PCIe slots 1-8 are connected to IOB0, and PCIe slots 9-16 are connected to IOB1.
- B. The order of PCIe card portion is the same in the OCU with 2x or 4x CMUs.
- C. In a 4x CMU environment, PCIe slot 3 shares the same PCIe switch with the EMS (Base I/O board); the same is true for PCIe slots 6,11, and 14.
- D. In 2x CMU environments, PCIe slots 3 and 4 share the same PCIe switch with the EMS I/O board); the same is true for PCIe slots 5 and 6, PCIe slots 11 and 12, and PCI slots 13 and 14.
- E. In the DCU with 2x CMUs (in slots 0 and 3), you may only populated card to PCIe slots 1-8.

**Answer: B,C,E**

**Question No : 13**

You need to record a system's serial number. What command do you run to see it?

- A. show /System model
- B. show /SP network
- C. show /System serial\_number
- D. show /SP hostname
- E. show /System part number

**Answer: C**

**Question No : 14**

Which three statements are correct about data paths in the SPARC M5-32 server?

- A. Within a DCU, a CPU talks to another CPU directly by using the coherency switch. The SSB is not needed.

- B. From one DCU to a different DCU, an even talks with only another even (same for odd) and hops to the odd-numbered board, if needed, on the destination DCU.
- C. From one DCU to a different DCU, all CPUs can access all PCIe slots In the destination DCU directly in one hop.
- D. A DCU can communicate data only with other DCUs via the BX ASICs on the SSB.
- E. From one DCU to a different DCU, even CPUs can access ail the odd PCIe slots in the destination DCU directly in one hop.

**Answer: A,C,D**

Reference:

[http://docs.oracle.com/cd/B10501\\_01/server.920/a96533/statspac.htm](http://docs.oracle.com/cd/B10501_01/server.920/a96533/statspac.htm)

### Question No : 15

You want Domain 3 to stop at the OBP when you power it on so that you can create OBP aliases. Which two ILOM commands will ensure this?

- A. set auto-boot? false
- B. set /Servers/PDomains/PDomain3/HOST/bootmode script= "setenv auto- boot? False"
- C. set /HOST3/auto-boot? false
- D. set /HOST3/bootmode script="setenv auto-boot? false"

**Answer: B,C**

### Question No : 16

Which three statements are true about the SPARC M5-32 server power grid connection?

- A. The server is designed to be powered by two utility power grids.
- B. You must connect three power cords (ACO, AC1, and AC2) to one power grid, and connect the remaining three power cords to a second power grid (AC3, AC4, and AC5).
- C. Only 3x power cords need to be connected when operating the server at half load.
- D. All six power cords must be connected, even if you are using only a single power grid.
- E. With dual-power feed set up, the first set of three power cords connected to the server are used to supply power, and the second set of three power cords are in standby mode.

**Answer: A,B,E**

Reference: